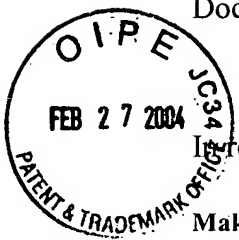


Docket No.: 67162-028

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Makoto KIDARA

Serial No.: 10/694,785

Group Art Unit: 2825

Filed: October 29, 2003

Examiner: To be Assigned

For: CIRCUIT SIMULATION FOR A CIRCUIT INCLUDING TRANSISTORS

PRELIMINARY AMENDMENT

Mail Stop
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as follows:

IN THE SPECIFICATION: (BRIEF EXPLANATION OF THE DRAWINGS)